

FIG. 1 is a block diagram of a system 100. The system 100 includes an Embedded Core 110, an OCM 120, and two G-Logic & Intercon. blocks 115-A and 115-B. The Embedded Core 110 has an OUT port and an IN port. The OCM 120 has an IN port and an OUT port. The G-Logic & Intercon. block 115-A has a dashed input and a dashed output. The G-Logic & Intercon. block 115-B has a dashed input and a dashed output. The system 100 is connected to two Clock 109 inputs. The system 100 is also connected to an Output Path 114 and an Input Path 113. The system 100 is further connected to a Core Output 121, a Core Input 133, a GL&I Output 122, a GL&I Output 132, and an OCM Input 123. The system 100 is also connected to an OCM Output 131.

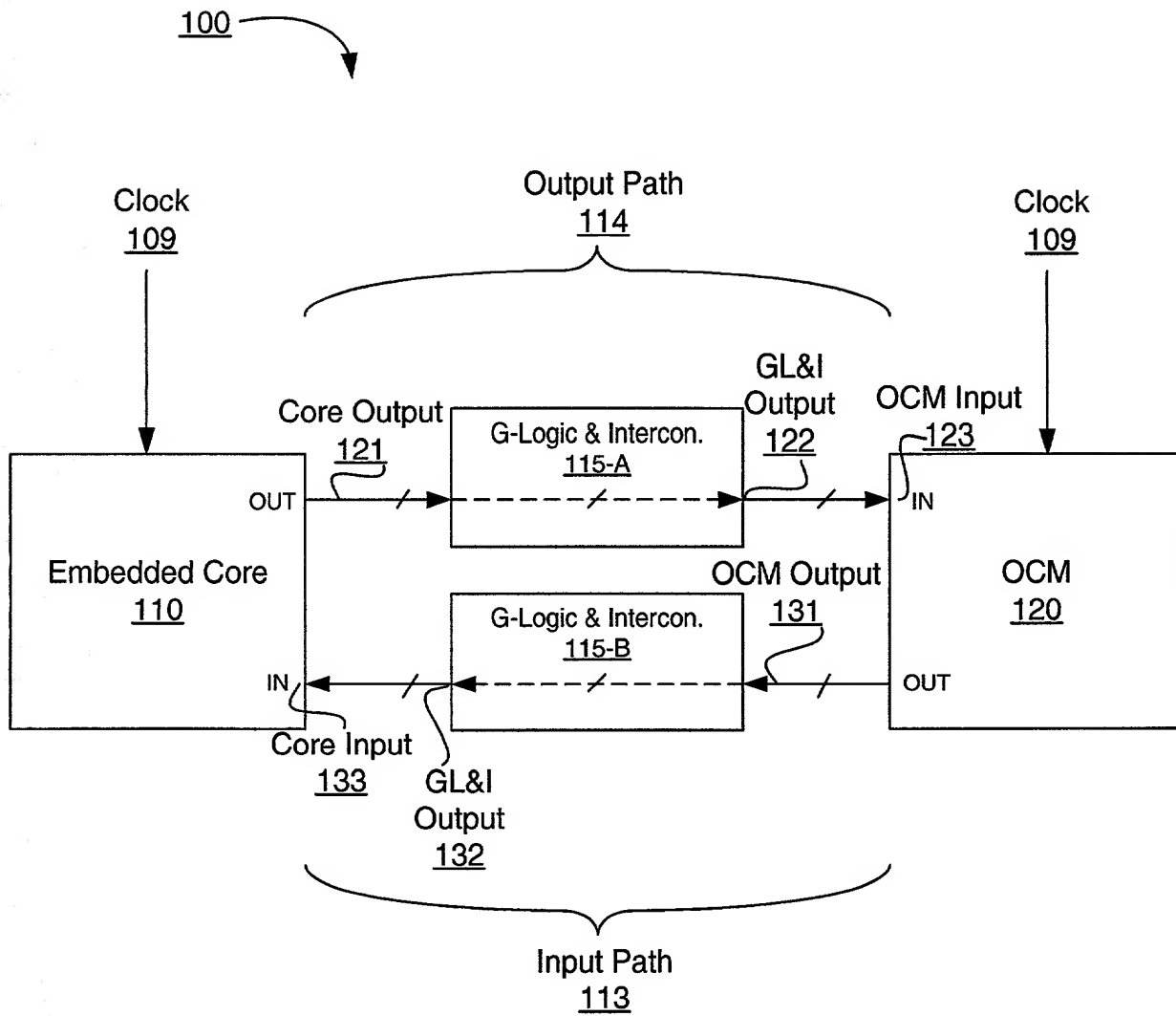


FIG. 1

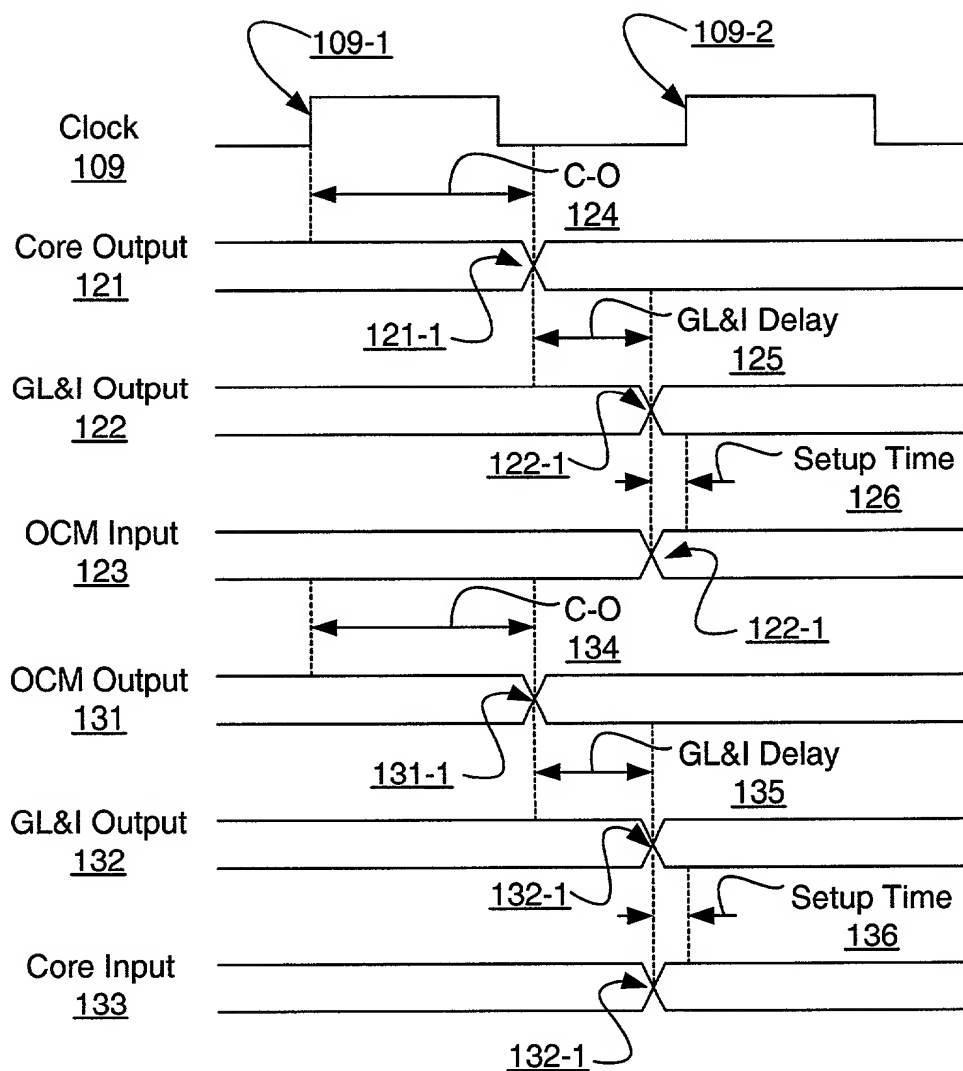


FIG. 2

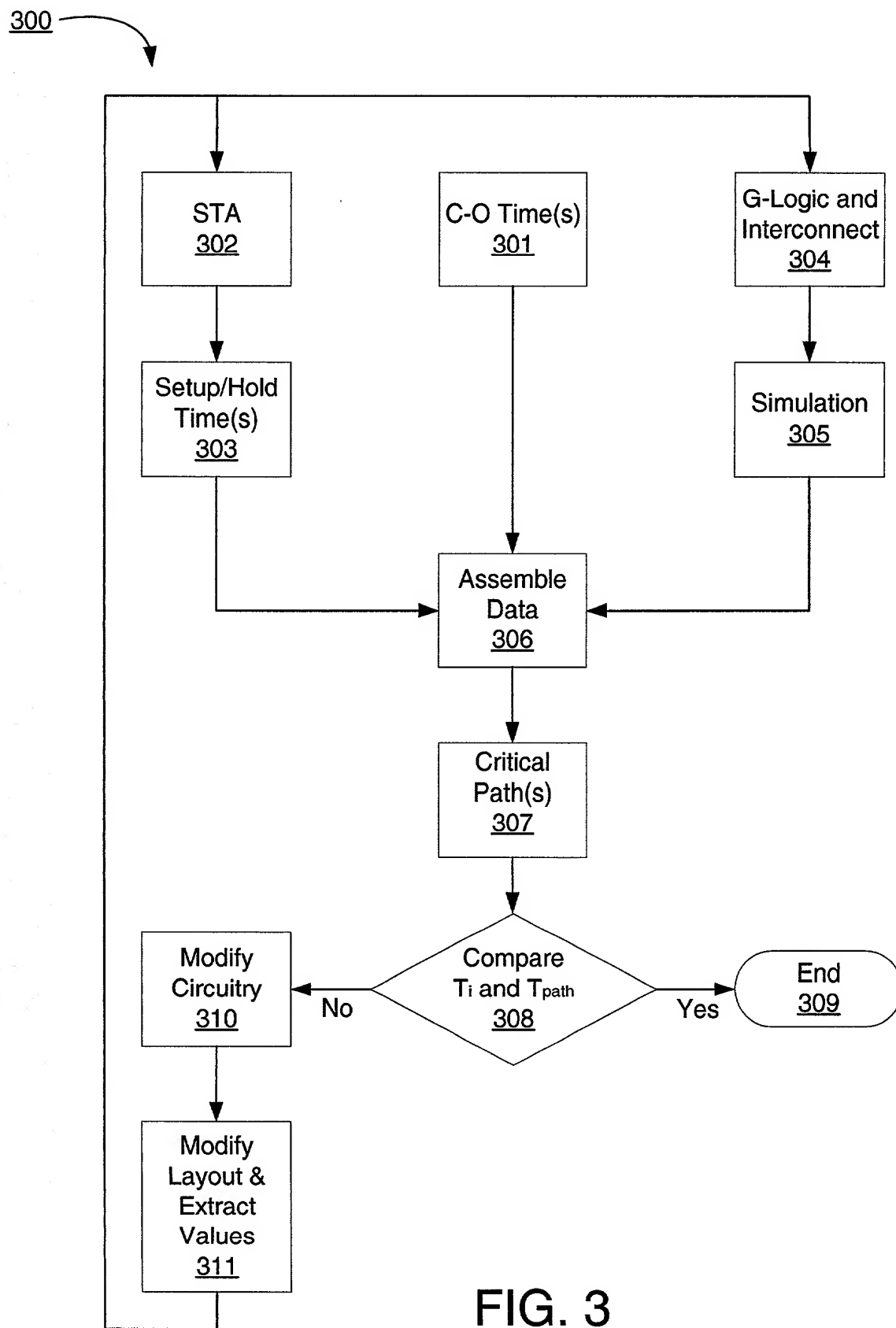


FIG. 3

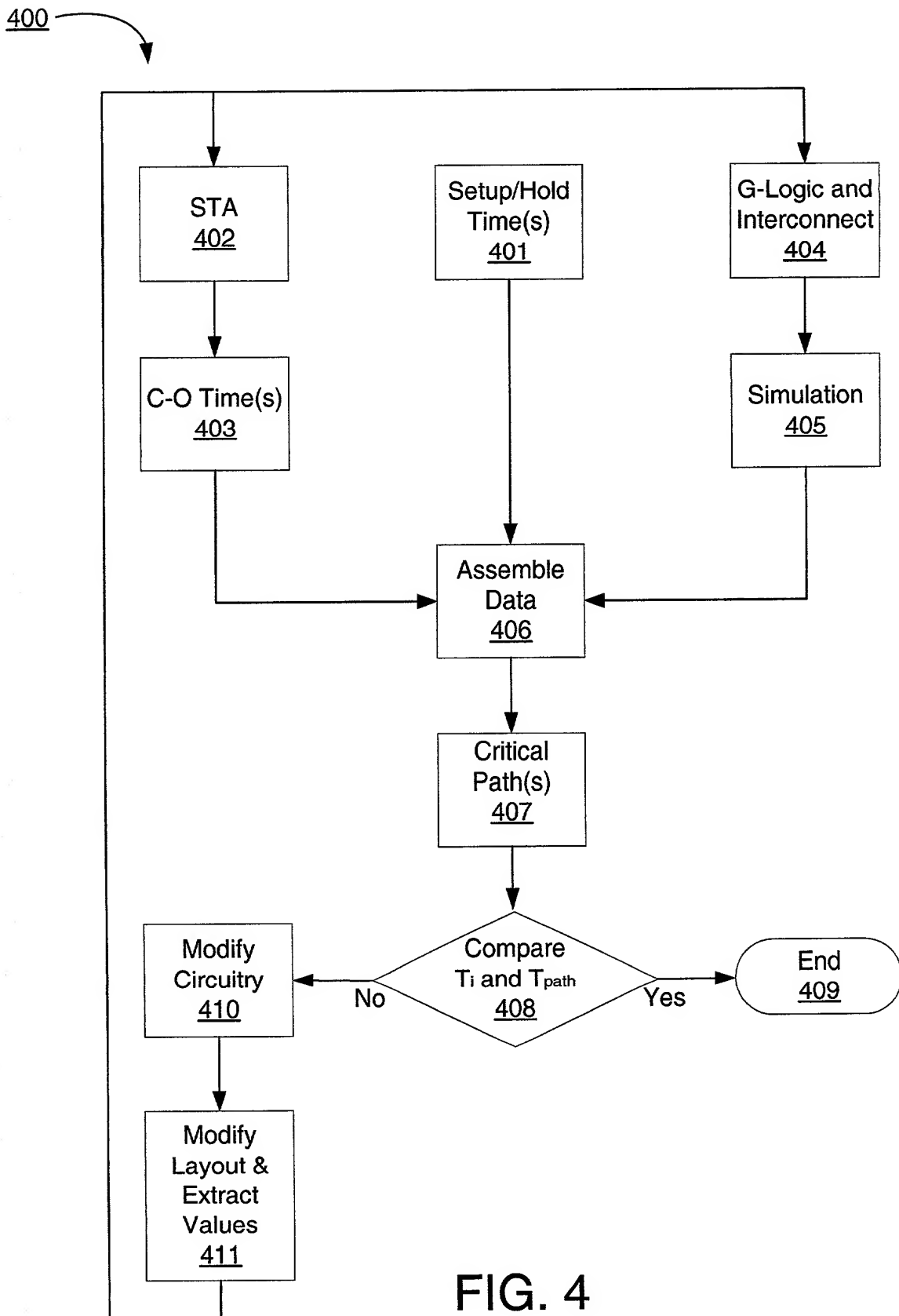


FIG. 4